

The image displays two views of the Mutec MC-7 Word Clock Distribution Amplifier & Audio Clock Converter. The top view shows the front panel, which is dark blue with silver and red accents. It features a Mutec logo, a title, and a 24/192 frequency specification. Controls include a red Power button, black Menu, Select, and Reference buttons, and a grid of green LED indicators for clock multipliers (x1 to x256). Status LEDs for WCLK, AES3/11, S/P-DIF co, and S/P-DIF op are also present. The bottom view shows the rear panel, which is black. It features various connectors: three WCLK OUTs (1, 2, 3), S/P-DIF OUT 6, AES3/11 OUT 5, AES3/11 IN, S/P-DIF IN, WCLK IN, and a power input. The Mutec logo and website are also visible on the rear panel.

MC-7

WORD CLOCK DISTRIBUTION AMPLIFIER & AUDIO CLOCK CONVERTER

SAFETY INSTRUCTIONS

General instructions

To reduce the risk of fire or electrical shock, do not expose this appliance to rain or moisture, direct sunlight or excessive heat from sources such as radiators or spotlights. No user serviceable parts are inside. Repair and maintenance must be carried out by qualified personnel authorized by MUTECH GmbH! The unit has been designed for operation in a standard domestic environment. Do NOT expose the unit and its accessories to rain, moisture, direct sunlight or excessive heat produced by such heat sources as radiators or spotlights! The free flow of air inside and around the unit must always be ensured.

Initial operation

Prior to the initial operation of the unit, the appliance, its accessories and packaging must be inspected for any signs of physical damage that may have occurred during transit. If the unit has been damaged mechanically or if liquids have been spilled inside the enclosure, the appliance may not be connected to the mains or must be disconnected from the mains immediately! If the unit is damaged, please do NOT return it to MUTECH GmbH, but notify your dealer and the shipping company immediately, otherwise claims for damage or replacement may not be granted.

If the device is left in a low-temperature environment for a long time and then is moved to a room-temperature environment, condensation may occur on the inside and the exterior. To avoid short-circuits and flashovers, be sure to wait one or two hours before putting the device into operation.

Power supply

The device contains a self-adapting wide-range power supply supporting the majority of global standard line voltages within a range of 90...250 V, with no need for making adjustments. Make sure that your line-voltage source provides a supply voltage within the specified range. In addition, make sure that the device is properly grounded via the local electric installation.

Please use the enclosed power cord (see packaging) to connect the unit to the mains. Switch the unit off before you attempt to connect it to the mains. Connect the power cord to the unit, then to a standard 3-pin mains outlet. To draw the power cord, never pull on the cable but on the mains plug!

The unit must be grounded during operation!

For information on the power-inlet wiring, refer to the »Wiring of connectors« section in the appendix. Disconnect the device from the mains when not using it for an extended period!



This symbol, a flash of lightning inside a triangle, alerts you to the presence of uninsulated dangerous voltage inside the enclosure - voltage that may be sufficient to constitute a risk of shock.



This symbol, an exclamation mark inside a triangle, alerts you to important operating or safety instructions in this manual.

Declaration of Conformity

We herewith confirm that the product complies with the European Commission's standards on electromagnetic compatibility.

Interference emission: EN 50081-1, 1992
Resistance to interference: EN 50082-1, 1992

Presupposed as operation condition is that all clock outputs are connected with high-quality and good shielded BNC 75 ohms cable.



WARRANTY REGULATIONS

§1 Warranty

MUTECH GmbH warrants the flawless performance of this product to the original buyer for a period of two (2) years from the date of purchase. If any failure occurs within the specified warranty period that is caused by defects in material and/or workmanship, MUTECH GmbH shall either repair or replace the product free of charge within 90 days. The purchaser is not entitled to claim an inspection of the device free of charge during the warranty period. If the warranty claim proves to be justified, the product will be returned freight prepaid by MUTECH GmbH within Germany. Outside Germany, the product will be returned with the additional international freight charges payable by the customer. Warranty claims other than those indicated above are expressly excluded.

§2 Warranty transferability

This warranty is extended exclusively to the original buyer who bought the product from a MUTECH GmbH specialized dealer or distributor, and is not transferable to anyone who may subsequently purchase this product. No other person (retail dealer, distributor, etc.) shall be entitled to give any warranty promise on behalf of MUTECH GmbH.

§3 Warranty regulations

The return of the completed registration card, or online registration on one of the websites specified below, is a condition of warranty. Failing to register the device before returning it for repair will void the extended warranty.

- The serial number on the returned device must match the one stated on the registration card or entered during online registration. Otherwise, the device will be returned to the sender at the sender's expense.
- Any returned device must be accompanied by a detailed error description and a copy of the original sales receipt issued by a MUTECH dealer or distributor.
- The device must be returned free of shipping expenses and in the original package, if possible; otherwise, the sender has to provide comparably protective packaging.
- The sender is fully responsible for any damage or loss of the product when shipping it to MUTECH GmbH.

§4 Limitation of warranty

Damages caused by the following conditions are not covered by this warranty:

- Damages caused by every kind of normal wear and tear (e.g. displays, LEDs, potentiometers, faders, switches, buttons, connecting elements, printed labels, cover glasses, cover prints, and similar parts).
- Functional failure of the product caused by improper installation (please observe CMOS components handling instructions!), neglect or misuse of the product, e.g. failure to operate the unit in compliance with the instructions given in the user or service manuals.
- Damage caused by any form of external mechanical impact or modification.
- Damage caused by the user's failure to connect and operate the unit in compliance with local safety regulations.
- Damage caused by force majeure (fire, explosion, flood, lightning, war, vandalism, etc.).
- Consequential damages or defects in products from other manufacturers as well as any costs resulting from a loss of production.

Repairs carried out by personnel which is not authorized from MUTECH GmbH will void the warranty. Adaptations and modifications to the device made with regard to national, technical, or safety regulations in a country or of the customer do not constitute a warranty claim and should be set with MUTECH GmbH in advance.

§5 Repairs

To obtain warranty service, the buyer must call or write to MUTECH GmbH before returning the unit. All inquiries must be accompanied by a description of the problem and the original buyer's invoice. Devices shipped to MUTECH GmbH for repair without prior notice will be returned to the sender at the sender's expense. In case of a functional failure please contact:

MUTECH Gesellschaft fuer Systementwicklung und Komponentenvertrieb mbH
Siekeweg 6/8 • 12309 Berlin • Germany • Fon 030-746880-0 • Fax 030-746880-99 • Tecsupport@MUTECH-net.com • www.MUTECH-net.com

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Peripheral MUTECH Products

Reference Clocks and Master Clocks for Synchronization:

- **iCLOCK + iCLOCKdp**
iCLOCK and iCLOCKdp are synchronizable, high-precision clock generators which are designed to be the reference in digital audio and video studios as well as broadcast and television stations. For further details please visit:
www.iCLOCK-NET.de
- **MC-3**
The MC-3 SMART CLOCK is an universal digital audio master clock generator. The unit provides different high-stable and Ultra low-jitter clock signals for synchronization of various digital audio devices.
- **MC-3.1**
The MC-3.1 SMART CLOCK SD is an universal digital audio and SD video sync master clock generator. The unit provides different high-stable clock signals for simultaneous synchronization of digital audio and SD video devices.
- **MC-3.2**
The MC-3.2 SMART CLOCK HD is an universal digital audio and SD/HD video sync master clock generator. The unit provides different high-stable clock signals for simultaneous synchronization of digital audio and SD/HD video devices.

Format and Sampling Rate Converters with internal Master Clock:

- **MC-4**
The MC-4 is a high-performance digital audio multichannel format and sampling rate converter for ADAT™, AES3 and S/P-DIF
- **MC-6**
The MC-6 is a high-performance digital audio dual channel format converter for AES3, AES3id and S/P-DIF.

Cables for Digital Audio:

- Optical cables in different lengths from 0.5 m to 20 m for ADAT™ transfers.

For all peripheral products please have a look on our website:
www.MUTECH-NET.com !



- Refer to the OPERATIONS chapter for more information.

MC-7 Rear Panel



For detailed specifications on all terminals, refer to the »Pin Assignment of the Connectors« and »Technical Data« in the chapter »APPENDIX«.

1 WCLK OUT 1-3

These 3 pairs of Word Clock outputs transfer either all standard Word Clock rates as well as so-called »Super Clock« rates (Word Clockx256) for older Digidesign ProTools™ systems. Their numbering is aligned to the functional menu »CLK MULTIPLIERS« on the front panel. The individual BNC connectors of an output pair are marked as A and B. This allows, for example, for a simple documentation of the connected devices. For adjusting these outputs see chapter OPERATING THE MC-7.

2 S/P-DIF OUT 6

These two S/P-DIF outputs, available as optical (»OP«) and coaxial (»CO«) interfaces, transmit an optical S/P-DIF and an unbalanced electrical S/P-DIF digital audio or blank frame signal in compliance with the IEC 60958 standard. Their numbering is aligned to the corresponding functional menu on the front panel. For adjusting these outputs see chapter OPERATING THE MC-7.

3 AES3/11 OUT 5

This AES/EBU output transmits a transformer-balanced electrical AES3 or AES11 signal. Its numbering is aligned to the corresponding functional menu on the front panel. For adjusting this output see chapter OPERATING THE MC-7.

4 AES3/11 IN

This AES/EBU input can receive a balanced digital AES3 or AES11 signal in compliance with AES3-1992 (R1997) or AES11-1997/2003. The input impedance is 110Ω (XLR connector, female). For selecting this input see chapter OPERATING THE MC-7.

5 S/P-DIF IN

These two S/P-DIF inputs, available as optical (»OP«) and coaxial (»CO«) interfaces, receive an optical S/P-DIF and an unbalanced electrical S/P-DIF digital audio or blank frame signal in compliance with the IEC 60958 standard. For selecting one of these inputs see chapter OPERATING THE MC-7.

6 WCLK IN

This input can receive a Word Clock or so-called »Super Clock« (Word Clockx256) signal. The impedance of the connector is 75Ω (BNC connectors, female). For selecting this input see chapter OPERATING THE MC-7.

7 MAINS IN, Power Switch + Power Inlet

This is the main switch for switching the device on and off. Be sure to make all connections (especially the supplied power cable) properly before turning on the switch. Heed the SAFETY INSTRUCTIONS at the beginning of this manual.

Connect the supplied power cable here. Make sure that the power switch is turned off before connecting the power cable to this inlet and to the power outlet. Line voltages within the range of 90...260V with a frequencies between 47...440Hz can be applied. The internal power supply will automatically make all necessary adjustments.



Since some manufacturers offer optimized cables for the transmission of digital S/P-DIF and AES/EBU audio signals, it will be a good idea to ask your retailer for specific cables.



Especially when working with high AES/EBU clock rates well shielded clock lines are imperative to avoid increased radiation! Standard cables are normally useable for clock rates up to 50.0kHz. Special shielded cable material should be used for transfer of higher clock rates.

MUTEC offers optical cables of various lengths that have been specifically tested for the transmission of S/P-DIF and ADAT™ signals (retailers and distributors only)!

Wiring the AES/EBU and S/P-DIF Interfaces

Connect the AES/EBU interfaces with the help of balanced electrical cables equipped with XLR connectors on both ends. The specifications stipulate a specific cable resistance of 110Ω (ask your retailer for a confirmation of this value when purchasing the cables).

Connect the coaxial S/P-DIF interface with the help of unbalanced electrical cables equipped with cinch connectors on both ends. The specifications stipulate a specific cable resistance of 75Ω (ask your retailer for a confirmation of this value when purchasing the cables).

Connect the optical S/P-DIF interface with the help of Toshiba TOSLINK™ compliant optical fiber cables. Here, you can use both plastic and glass fiber-based cables. When using plastic fiber cables, lengths of 10 meters should not be exceeded, so as to ensure the reliable transmission of signals. Glass fiber cables can transfer data reliably even over greater distances.



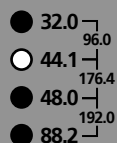




Locking so-called »Super Clocks«

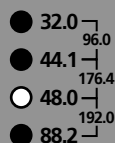
Your MC-7 is able to lock to so-called »Super Clock« (SCLK) reference signals. These clock signals are used preferably for older digidesign ProTools™ MX systems. Specified are only two clock rates, 11.2896MHz + 12.288MHz which are the x256 multiples of the Word Clock rates 44.1kHz and 48.0kHz.

When locking to one of these Super Clocks, the rate will be inverted displayed in the »REF CLOCK IN« menu. Due to this, the LED in front of the corresponding base clock rate, that means Word Clock rate, does not light while all other LEDs light (see examples below).



CLOCK IN

Super Clock of 44.1kHz Word Clock



CLOCK IN

Super Clock of 48.0kHz Word Clock

Differences between AES3 and AES11

Both standards, published by the Audio Engineering Society (AES), are based on the same interface, commonly called as AES/EBU interface which uses XLR connectors.

The AES3 standard carries both, the digital audio data and the reference clock information. The AES11, also known as 'blank frame signal', does not carry any digital audio data, but only the reference clock information for synchronization.

Word Clock as Input Reference including Options

	1	2	3	4	5	6
● WCLK	●	●	●	●	●	●
○ AES3/11	○	○	○	○	○	○
○ S/P-DIF co	○	○	○	○	○	○
○ S/P-DIF op	○	○	○	○	○	○
REFERENCE	CLK MULTIPLIERS					

This setting is the factory default and allows to input a Word Clock reference signal between 32.0kHz and 192.0kHz. Its clock rate will be displayed in the status menu »CLOCK IN«. The Word Clock reference signal will be internally regenerated and distributed phase-aligned to all eight Word Clock outputs. Simultaneously, the input signal will be converted into AES11 and S/P-DIF and output at the corresponding outputs. So, with one Word Clock reference signal you can synchronize various devices by Word Clock, AES11, S/P-DIF coaxial and S/P-DIF optical at the same time.

	1	2	3	4	5	6
● WCLK	●	●	●	●	●	●
○ AES3/11	○	○	○	○	○	○
○ S/P-DIF co	○	○	○	○	○	○
○ S/P-DIF op	○	○	○	○	○	○
REFERENCE	CLK MULTIPLIERS					

A special function can be added when pressing the SELECT key for a second time. This activates the »HOLD« function which leads to a fail-safe output of all outgoing signals. Interruptions within the input reference signal or even its total loss will not affect the continuous availability of all outgoing signals.

AES3/11 as Input Reference including Options

	1	2	3	4	5	6
○ WCLK	○	○	○	○	○	○
● AES3/11	●	●	●	●	●	●
○ S/P-DIF co	○	○	○	○	○	○
○ S/P-DIF op	○	○	○	○	○	○
REFERENCE	CLK MULTIPLIERS					

This setting allows to input an AES3 digital audio signal or an AES11 reference clock signal between 32.0kHz and 192.0kHz. Its clock rate will be displayed in the status menu »CLOCK IN«. The AES3/11 input signal will be regenerated and converted into Word Clock and S/P-DIF and transferred to the corresponding outputs simultaneously.

If an AES3 digital audio signal is feed in, it will be standardly output as AES11 reference clock signal. Similarly, the digital audio data will be not transferred to the S/P-DIF outputs, but only the clock reference data for synchronization purposes.

	1	2	3	4	5	6
○ WCLK	○	○	○	○	○	○
● AES3/11	●	●	●	●	●	●
○ S/P-DIF co	○	○	○	○	○	○
○ S/P-DIF op	○	○	○	○	○	○
REFERENCE	CLK MULTIPLIERS					

There are two further function options available when feeding in an AES3 or AES11 signal: »HOLD« + »REF PASS«.

OPERATION

When pressing the SELECT key for a further time, you can activate the »HOLD« function which leads to a fail-safe output of all outgoing signals.

☐ WCLK	☐ HOLD	x1	☒	☒	☒	☒	☒	☒
☒ AES3/11	☒ REF PASS	x2	☐	☐	☐	☐	☐	☐
☐ S/P-DIF co		x4	☐	☐	☐	☐	☐	☐
☐ S/P-DIF op		x256	☐	☐	☐	☐		
REFERENCE		CLK MULTIPLIERS						

When pressing the SELECT key again, you can activate the »REF PASS« function. This special function regenerates and transfers an AES3 input signal to the format-same output. In this case, the AES3/11 output transmits the original AES3 input signal incl. all digital audio data for further use within your studio set-up, while the other outputs supplying phase-aligned clock reference signals.

S/P-DIF coaxial as Input Reference including Options

☐ WCLK	☐ HOLD	x1	☒	☒	☒	☒	☒	☒
☐ AES3/11	☐ REF PASS	x2	☐	☐	☐	☐	☐	☐
☒ S/P-DIF co		x4	☐	☐	☐	☐	☐	☐
☐ S/P-DIF op		x256	☐	☐	☐	☐		
REFERENCE		CLK MULTIPLIERS						

This setting allows to input a S/P-DIF digital audio or blank frame signal between 32.0kHz and 192.0kHz at the coaxial input. Its clock rate will be displayed in the status menu »CLOCK IN«. The S/P-DIF input signal will be regenerated and converted into Word Clock, AES11 and S/P-DIF optical and transferred to the corresponding outputs simultaneously.

A S/P-DIF digital audio input signal will be standardly output as S/P-DIF blank frame signal for synchronization purposes at the coaxial and optical outputs. Similarly, the digital audio data will be not transferred to the AES3/11 output, but only the clock reference data.

☐ WCLK	☒ HOLD	x1	☒	☒	☒	☒	☒	☒
☐ AES3/11	☐ REF PASS	x2	☐	☐	☐	☐	☐	☐
☒ S/P-DIF co		x4	☐	☐	☐	☐	☐	☐
☐ S/P-DIF op		x256	☐	☐	☐	☐		
REFERENCE		CLK MULTIPLIERS						

There are also two further function options available when feeding in an S/P-DIF coaxial signal: »HOLD« + »REF PASS«.

When pressing the SELECT key for a further time, you can activate the »HOLD« function which leads to a fail-safe output of all outgoing signals.

☐ WCLK	☐ HOLD	x1	☒	☒	☒	☒	☒	☒
☐ AES3/11	☒ REF PASS	x2	☐	☐	☐	☐	☐	☐
☒ S/P-DIF co		x4	☐	☐	☐	☐	☐	☐
☐ S/P-DIF op		x256	☐	☐	☐	☐		
REFERENCE		CLK MULTIPLIERS						

When pressing the SELECT key again, you can activate the »REF PASS« function. This special function regenerates and transfers a S/P-DIF coaxial input signal to the format-same output. In this case, the S/P-DIF output signal



AES3 + REF PASS

When feeding in an AES3 digital audio reference signal and using the REF PASS functionality, the digital audio signal will be only available at the format-same AES3 output. There will be no digital audio format conversion to the S/P-DIF outputs! The S/P-DIF outputs transmit blank frame signals only.*

While processing the AES3 signal, the PLL synthesizer circuit extracts the clock out of the AES3 signal, is re-generating it and is supplying it to all WCLK outputs. The WCLK output pairs can be multiplied individually by using the CLK Multipliers as mentioned at page 19.

*) If you need to convert the AES3 format into S/P-DIF, please use the MUTECH MC-1 or MC-1.1. These converters are especially made for AES3 to S/P-DIF, and vice versa, digital audio format conversion.



S/P-DIF coaxial + REF PASS

When feeding in a S/P-DIF coaxial digital audio reference signal and using the REF PASS functionality, the digital audio signal will be only available at the format-same S/P-DIF coaxial/optical outputs. There will be no digital audio format conversion to the AES3/11 output! The AES3/11 output transmits the AES11 blank frame signal only.*

While processing the S/P-DIF signal, the PLL synthesizer circuit extracts the clock out of the S/P-DIF signal, is re-generating it and is supplying it to all WCLK outputs. The WCLK output pairs can be multiplied individually by using the CLK Multipliers as mentioned at page 19.

*) If you need to convert the S/P-DIF format into AES3, please use the MUTECH MC-1 or MC-1.1. These converters are especially made for AES3 to S/P-DIF, and vice versa, digital audio format conversion.

includes all digital audio data for further use within your studio set-up, while the other outputs supplying phase-aligned clock reference signals.

S/P-DIF optical as Input Reference including Options

		1	2	3	4	5	6
☐ WCLK	☐ HOLD	x1 ☒	☒	☒	☒	☒	☒
☐ AES3/11	☐ REF PASS	x2 ☐	☐	☐	☐	☐	☐
☐ S/P-DIF co		x4 ☐	☐	☐	☐	☐	☐
☒ S/P-DIF op		x256 ☐	☐	☐	☐		
REFERENCE		CLK MULTIPLIERS					

This setting allows to input a S/P-DIF digital audio signal between 32.0kHz and 192.0kHz at the optical input. Its clock rate will be displayed in the status menu »CLOCK IN«. The S/P-DIF input signal will be regenerated and converted into Word Clock, AES11 and S/P-DIF coaxial and transferred to the corresponding outputs simultaneously.

A S/P-DIF digital audio input signal will be standardly output as S/P-DIF blank frame signal for synchronization purposes at the optical and coaxial outputs. Similarly, the digital audio data will be not transferred to the AES3/11 output, but only the clock reference data.

		1	2	3	4	5	6
☐ WCLK	☒ HOLD	x1	☒	☒	☒	☒	☒
☐ AES3/11	☐ REF PASS	x2	☐	☐	☐	☐	☐
☐ S/P-DIF co		x4	☐	☐	☐	☐	☐
☒ S/P-DIF op		x256	☐	☐	☐	☐	
REFERENCE		CLK MULTIPLIERS					

There are also two further function options available when feeding in an S/P-DIF optical signal: »HOLD« + »REF PASS«.

When pressing the SELECT key for a further time, you can activate the »HOLD« function which leads to a fail-safe output of all outgoing signals.

S/P-DIF optical + REF PASS

! When feeding in a S/P-DIF optical digital audio reference signal and using the REF PASS functionality, the digital audio signal will be only available at the format-same S/P-DIF coaxial/optical outputs. There will be no digital audio format conversion to the AES3/11 output! The AES3/11 output transmits the AES11 blank frame signal only.*

While processing the S/P-DIF signal, the PLL synthesizer circuit extracts the clock out of the S/P-DIF signal, is re-generating it and is supplying it to all WCLK outputs. The WCLK output pairs can be multiplied individually by using the CLK Multipliers as mentioned at page 19.

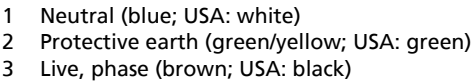
***) If you need to convert the S/P-DIF format into AES3, please use the MUTECH MC-1 or MC-1.1. These converters are especially made for AES3 to S/P-DIF, and vice versa, digital audio format conversion.**

		1	2	3	4	5	6
☐ WCLK	☐ HOLD	x1 ☒	☒	☒	☒	☒	☒
☐ AES3/11	☒ REF PASS	x2 ☐	☐	☐	☐	☐	☐
☐ S/P-DIF co		x4 ☐	☐	☐	☐	☐	☐
☒ S/P-DIF op		x256 ☐	☐	☐	☐		
REFERENCE		CLK MULTIPLIERS					

When pressing the SELECT key again, you can activate the »REF PASS« function. This special function regenerates and transfers a S/P-DIF optical input signal to the format-same output. In this case, the S/P-DIF output signal includes all digital audio data for further use within your studio set-up, while the other outputs supplying phase-aligned clock reference signals.

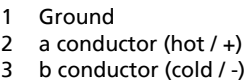


Mains

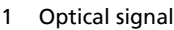


1	Signal
2	Ground

1 Audio ground
2 a conductor (hot / +)
3 b conductor (cold / -)



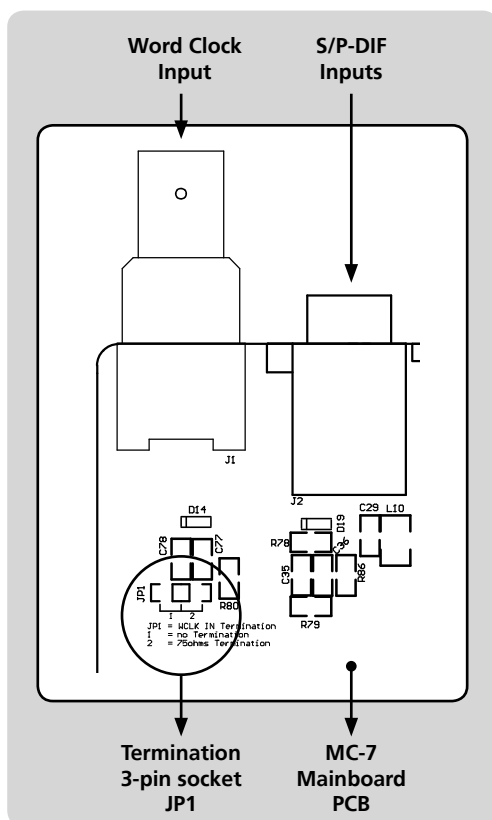
1 Audio signal
2 Audio ground



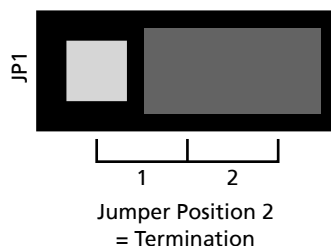
Switching-off the Termination of the Word Clock Input

CAUTION! Disconnect the unit from the mains before opening!
Remount the aluminium cover thoroughly before you attempt to operate the unit!

When MC-7 is shipped, the BNC-based Word Clock input connector is terminated internally with 75Ω . Therefore, one jumper is put on two pins - Position 2 - of the 3-pin socket JP1.



Word Clock Termination

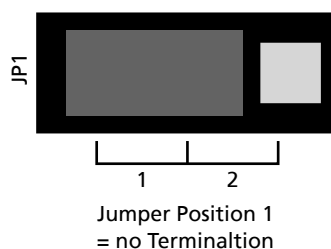


Jumper:



Free Pin:

When moving the jumper from position 2 to position 1, the input termination will be switched-off. Therefore, the MC-7 must be connected in a chain, in which a device with terminated input follows. Otherwise you need to use a BNC-T piece in combination with a 75Ω BNC resistor for terminating the MC-7's input.



For additional information regarding this issue, please refer to page 11.

WORD CLOCK SIGNAL AS INPUT REFERENCE

WORD CLOCK SIGNAL AS INPUT REFERENCE + HOLD FUNCTION

AES3 DIGITAL AUDIO SIGNAL AS INPUT REFERENCE

AES3 DIGITAL AUDIO SIGNAL AS INPUT REFERENCE + HOLD FUNCTION

AES3 DIGITAL AUDIO SIGNAL AS INPUT REFERENCE + REF PASS FUNCTION

AES11 BLANK FRAME SIGNAL AS INPUT REFERENCE

[illegible]

AES11 BLANK FRAME SIGNAL AS INPUT REFERENCE + HOLD FUNCTION					
Outputs	Multiply x 1	Multiply x 2	Multiply x 4	Multiply x 256	Output Signal
1: Word Clock, A+B	44.1kHz	88.2kHz	176.4kHz	Super Clock	Clock Signal
2: Word Clock, A+B	44.1kHz	88.2kHz	176.4kHz	Super Clock	Clock Signal
3: Word Clock, A+B	44.1kHz	88.2kHz	176.4kHz	Super Clock	Clock Signal
4: Word Clock, A+B	44.1kHz	88.2kHz	176.4kHz	Super Clock	Clock Signal
5: AES3/11	44.1kHz	88.2kHz	176.4kHz	None	AES11, blank frame, none audio
6: S/P-DIF op+co	44.1kHz	88.2kHz	176.4kHz	None	S/P-DIF blank frame, none audio

S/P-DIF COAXIAL DIGITAL AUDIO SIGNAL AS INPUT REFERENCE					
Outputs	Multiply x 1	Multiply x 2	Multiply x 4	Multiply x256	Output Signal
1: Word Clock, A+B	44.1kHz	88.2kHz	176.4kHz	Super Clock	Clock Signal
2: Word Clock, A+B	44.1kHz	88.2kHz	176.4kHz	Super Clock	Clock Signal
3: Word Clock, A+B	44.1kHz	88.2kHz	176.4kHz	Super Clock	Clock Signal
4: Word Clock, A+B	44.1kHz	88.2kHz	176.4kHz	Super Clock	Clock Signal
5: AES3/11	44.1kHz	88.2kHz	176.4kHz	None	AES11, blank frame, none audio
6: S/P-DIF op+co	44.1kHz	88.2kHz	176.4kHz	None	S/P-DIF blank frame, none audio

S/P-DIF COAXIAL DIGITAL AUDIO SIGNAL AS INPUT REFERENCE + HOLD FUNCTION					
Outputs	Multiply x 1	Multiply x 2	Multiply x 4	Multiply x256	Output Signal
1: Word Clock, A+B	44.1kHz	88.2kHz	176.4kHz	Super Clock	Clock Signal
2: Word Clock, A+B	44.1kHz	88.2kHz	176.4kHz	Super Clock	Clock Signal
3: Word Clock, A+B	44.1kHz	88.2kHz	176.4kHz	Super Clock	Clock Signal
4: Word Clock, A+B	44.1kHz	88.2kHz	176.4kHz	Super Clock	Clock Signal
5: AES3/11	44.1kHz	88.2kHz	176.4kHz	None	AES11, blank frame, none audio
6: S/P-DIF op+co	44.1kHz	88.2kHz	176.4kHz	None	S/P-DIF blank frame, none audio

S/P-DIF COAXIAL DIGITAL AUDIO SIGNAL AS INPUT REFERENCE + REF PASS FUNCTION					
Outputs	Multiply x 1	Multiply x 2	Multiply x 4	Multiply x256	Output Signal
1: Word Clock, A+B	44.1kHz	88.2kHz	176.4kHz	Super Clock	Clock Signal
2: Word Clock, A+B	44.1kHz	88.2kHz	176.4kHz	Super Clock	Clock Signal
3: Word Clock, A+B	44.1kHz	88.2kHz	176.4kHz	Super Clock	Clock Signal
4: Word Clock, A+B	44.1kHz	88.2kHz	176.4kHz	Super Clock	Clock Signal
5: AES3/11	44.1kHz	None	None	None	AES11, blank frame, none audio
6: S/P-DIF op+co	44.1kHz	None	None	None	S/P-DIF digital audio signal *

[illegible]

S/P-DIF OPTICAL DIGITAL AUDIO SIGNAL AS INPUT REFERENCE + HOLD FUNCTION					
Outputs	Multiply x 1	Multiply x 2	Multiply x 4	Multiply x 256	Output Signal
1: Word Clock, A + B	44.1kHz	88.2kHz	176.4kHz	Super Clock	Clock Signal
2: Word Clock, A + B	44.1kHz	88.2kHz	176.4kHz	Super Clock	Clock Signal
3: Word Clock, A + B	44.1kHz	88.2kHz	176.4kHz	Super Clock	Clock Signal
4: Word Clock, A + B	44.1kHz	88.2kHz	176.4kHz	Super Clock	Clock Signal
5: AES3/11	44.1kHz	88.2kHz	176.4kHz	None	AES11, blank frame, none audio
6: S/P-DIF op + co	44.1kHz	88.2kHz	176.4kHz	None	S/P-DIF blank frame, none audio

S/P-DIF OPTICAL DIGITAL AUDIO SIGNAL AS INPUT REFERENCE + REF PASS FUNCTION					
Outputs	Multiply x 1	Multiply x 2	Multiply x 4	Multiply x 256	Output Signal
1: Word Clock, A + B	44.1kHz	88.2kHz	176.4kHz	Super Clock	Clock Signal
2: Word Clock, A + B	44.1kHz	88.2kHz	176.4kHz	Super Clock	Clock Signal
3: Word Clock, A + B	44.1kHz	88.2kHz	176.4kHz	Super Clock	Clock Signal
4: Word Clock, A + B	44.1kHz	88.2kHz	176.4kHz	Super Clock	Clock Signal
5: AES3/11	44.1kHz	None	None	None	AES11, blank frame, none audio
6: S/P-DIF op + co	44.1kHz	None	None	None	S/P-DIF digital audio signal *

*) Both S/P-DIF outputs are supplying the same digital audio signal with same content, but electrically converted into a coaxial and an optical signal.

[illegible]



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