



## LINK ELECTRONICS, INC.

### **HD 3G SD ASI DIGITAL DISTRIBUTION AMPLIFIER**





#### **FEATURES**

#### **Model MDA-55**

- ◆ DC Powered, Portable
- ◆ SMPTE 259M, 344M, 292M compliant
- Wall Wart Power Supply
- ◆ HD SD SDI 3G & ASI Input

The VDA-55 HD SD SDI & ASI distribution amplifiers combine exceptional performance with both value and compact size. The MDA-55 supports 143Mb/s, 270Mb/s and 3Gb/s Auto Rate Detection with "Power OK", "Lock" and "HD" indicators. The compact size of this distribution amplifier allows it to be located in spaces where standard rack space is limited.

The LINK MDA-55 HD SD SDI & ASI DA restores the incoming signal to its proper amplitude. The signal is reshaped with automatic equalization and DC restoration. The MDA-55's maximum equalized cable length; for SD up to 250 feet at 270Mb/s, and HD up to 100 feet at 1.485Gb/s. Four outputs are provided on the end panel, as well as, a detachable DC power cable.

There are three LED's, on the end opposite the BNC connectors, showing Lock, Signal Presence and DC Power. There are four outputs that represent the input.

The MDA-55 input signal can be 143Mb/s, 177Mb/s, 270Mb/s, 360Mb/s, 540Mb/s, and 3 Gb/s standards.

The MDA-55 carries a 10-year warranty including parts and labor.

- ◆ Four HD SD SDI 3G & ASI Outputs
- ◆ Lock/Format Indication
- Automatic Equalization and Re-clocking
- Compact Size

The three LED's show the status of the input signal, locked, and whether the data is Serial Digital standard definition or High Definition, HD SDI. The signal output level is 800mV p-p, and reclocked. The MDA-55 can be used in places where space is a premium and at the same time get excellent performance and lower cost.

The MDA-55 is designed for applications that require equalization, re-clocking and distribution for HD SD SDI digital video as per SMPTE 259 (SDI) and SMPTE 292 (HD). The MDA-55 includes an automatic input cable compensation circuit commonly referred to as an adaptive cable equalizer.

The MDA-55 includes a crystal referenced reclocking circuit that senses three data rates: 143 Mb/s, 270Mb/s and 1.484 Gb/s &3Gb/s. If the data rate is one of these three rates, the re-clocker will automatically activate and reduce high frequency jitter. If the incoming data rate is not 143 Mb/s, 270Mb/s or 1.484 Gb/s, then the MDA-55 reclocker will go into the bypass mode. If the data rate is 143 Mb/s or 270 Mb/s, the MDA-55 output rise times are slowed down to comply with the SMPTE 259 specification. The default for the MDA-55 is the faster SMPTE 292 HD rise time.

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# MDA-55 SERIAL DIGITAL DISTRIBUTION AMPLIFIER SPECIFICATIONS

### **Serial Digital Video Input:** Connectors: . . . . . . . . . . . BNC Standards: Input Cable Length Max: **Digital Outputs:** Standards: . . . . . Same as input **End Panel LED:** Lock Detect: LED HD:..... LED DC Power: . . . . . LED **ENVIRONMENTAL: MECHANICAL:**



BNC VIEWING ANGLE